

<b>Notice of References Cited</b>	Application/Control No.	Applicant(s)/Patent Under Reexamination YEO ET AL.
	Examiner Christopher W Lattin	Art Unit 2812

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-US006111267A	08-2000	Fischer et al.	257/19
	B	US-US006190975B1	02-2001	Kubo et al.	438/285
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, S.; Silicon Processing for the VLSI Era Volume 3: The Submicron MOSFET, Sunset Beach, CA, 1995, p.p. 367-373.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.